



GAU 2811

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PTO/SB/21 (08-00)  
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<b>TRANSMITTAL FORM</b> <i>(to be used for all correspondence after initial filing)</i>	<b>Application Number</b>	09/535,836	
	<b>Filing Date</b>	March 28, 2000	
	<b>First Named Inventor</b>	Hisashi Ohtani	
	<b>Group Art Unit</b>	2811	
	<b>Examiner Name</b>	Hung K. Vu	
<b>Total Number of Pages in This Submission</b>	5	<b>Attorney Docket Number</b>	SEL 173

ENCLOSURES <i>(check all that apply)</i>		
<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Assignment Papers <i>(for an Application)</i>	<input type="checkbox"/> After Allowance Communication to Group
<input type="checkbox"/> Fee Attached	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences
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<input type="checkbox"/> After Final	<input type="checkbox"/> Petition	<input type="checkbox"/> Proprietary Information
<input type="checkbox"/> Affidavits/declaration(s)	<input type="checkbox"/> Petition to Convert to a Provisional Application	<input type="checkbox"/> Status Letter
<input type="checkbox"/> Extension of Time Request	<input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address	<input type="checkbox"/> Other Enclosure(s) <i>(please identify below):</i>
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<input type="checkbox"/> Certified Copy of Priority Document(s)	<input type="checkbox"/> CD, Number of CD(s) _____	
<input type="checkbox"/> Response to Missing Parts/ Incomplete Application	<b>Remarks</b>	
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Attorney Docket SEL 173 *ele.*

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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*4/25/01*  
*Surles*

In Re Application of

Ohtani et al.

Serial No.: 09/535,836

Filed: March 28, 2000

For: Semiconductor Device And  
The Fabricating Method Thereof

Art Unit: 2811

Examiner: H. Vu

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**ELECTION AND AMENDMENT A**

01 IC:103 216.00 CH  
02 FC:102 Sir:60.00 CH

In response to the Restriction Requirement of March 19, 2001, Applicants elect to prosecute

Group I: Claims 1-12 and 19-24 in the above-identified application. Applicants are making this election without prejudice to later filing a divisional application on the non-elected claims.

Please also amend the above-identified application as follows:

**IN THE CLAIMS:**

Please add the following new claims:

*Sub*  
*But*  
*21*

28. A semiconductor device comprising:

a thin film transistor formed over a substrate, said thin film transistor having a semiconductor layer and a gate electrode adjacent to said semiconductor layer with a gate insulating film interposed therebetween;